

**IN THE U.S. PATENT AND TRADEMARK OFFICE**

In re application of

Yasuhiko KASAMA et al.

Conf. 9839

Application No. 10/577,526

Group 2811

Filed March 16, 2007

Examiner Trang Q. Tran

COMPLEMENTARY MISFET AND INTEGRATED CIRCUIT

**PETITION FOR EXTENSION OF TIME**

Assistant Commissioner for Patents

November 25, 2008

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

The undersigned hereby petitions for an extension of time to respond to the Official Action of July 25, 2008 for three months to November 25, 2008.

Please charge the extension fee of \$555.00 to our credit card.

A responsive paper is filed herewith.

Respectfully submitted,

YOUNG & THOMPSON

/Robert J. Patch/

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